

Shaurya Shubham

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PROFILE

I am an undergraduate student at the University of Wisconsin-Madison, studying Computer Engineering. I am proficient in software development and digital hardware design, and I love to code. My website shauryashubham.com showcases some of the code, projects and technical articles I have written.

PROJECTS

KNIGHT'S TOUR ROBOT

- Created as a part of class in ECE 551
- Controlled a robot to cover every square on a 5x5 chessboard using only knight moves using SystemVerilog
- Wrote module-level and top-level testbenches to comprehensively test the digital logic
- Synthesized logic into Synopsys's cell library and successfully did post-synthesis testing

GUITAR TUNER

- Created as a part of class in ECE 352
- Displayed the difference between the measured audio frequency and a changeable reference frequency with digital logic written in SystemVerilog
- Successfully synthesized and tested on an Altera DE-2 FPGA

CHESS GAME

- Created a cross-platform GUI application using Python and PyQt
- Stored game data in a local database using SQLite

EDUCATION

UNIVERSITY OF WISCONSIN-MADISON

Bachelor of Science in Computer Engineering

MADISON, WI

May 2027

- Cumulative GPA: 3.4/4.0
- Relevant coursework:
 - Signal Processing (ECE 203)
 - Software Programming and Testing (CS 300)
 - Digital System Fundamentals (ECE 352)
 - Data Structures and Algorithms (CS 400)
 - Electroacoustical Engineering (ECE 401)
 - Computer Networking (ECE 537)
 - Digital System Design & Synthesis (ECE 551)
 - Microprocessors (ECE 353)

SKILLS

Languages English, Hindi

Programming Languages Rust, Java, Python, JavaScript, MATLAB

Hardware Description Languages SystemVerilog, Verilog

Technical skills SQL, Qt, Linux, Git, Mercurial(hg)